

**RESPONSE UNDER 37 C.F.R. §1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2826**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Seiji Funaba, et al.	Examiner:	Benjamin P. Sandvik
Serial No.:	10/672,551	Art Unit:	2826
Filed:	September 26, 2003	Docket:	17072
For:	SEMICONDUCTOR UNIT HAVING TWO DEVICE TERMINALS FOR EVERY ONE INPUT/OUTPUT SIGNAL	Dated:	April 2, 2008

Confirmation No.: 3724

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

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Sir:

In response to a final Official Action mailed January 2, 2008, and in accordance with the provisions of 37 C.F.R. §1.116, Applicants submit this response and respectfully request entry thereof in the above-identified application.

CERTIFICATION OF ELECTRONIC TRANSMISSION

I hereby certify that this paper is being electronically transmitted to the Patent and Trademark Office on the date shown below.

Dated: April 2, 2008



Paul J. Esatto, Jr.